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SP  
2133  
7-23-03

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventors: Michael C. Dorsey

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Atty. Dkt. No.: 5681-55200

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Serial No.: 09/976,490

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Examiner: Unknown

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Art Unit: 2133

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Filing Date: October 12, 2001

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For: ASIC BIST Controller Employing  
Multiple Clock Domains

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I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below:

B. Noel Kivlin

Name of Registered Representative

7-11-03

Date

[Signature]

Signature

**SUBMISSION OF FORMAL DRAWINGS**

**Official Draftsman**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**RECEIVED**

JUL 17 2003

Technology Center 2100

Sir:

Applicant hereby submits the formal drawings for the above-referenced application and requests that these drawings be accepted for filing.

Respectfully submitted,

[Signature]

B. Noël Kivlin

Registration No. 33,929

Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.

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Date: 7-11-03